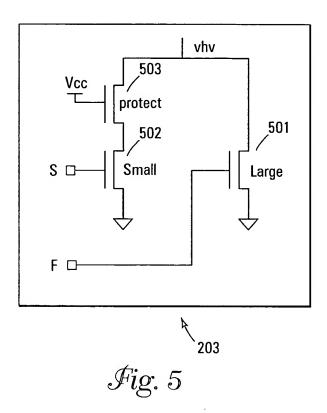


Fig. 4



DC block logic table.

enable	Disch fast		S	F
0 0 1 1	0 1 0 1		1 1 0 0	0 1 0 0
	<b>1</b> :	<b>,</b>	_	•

